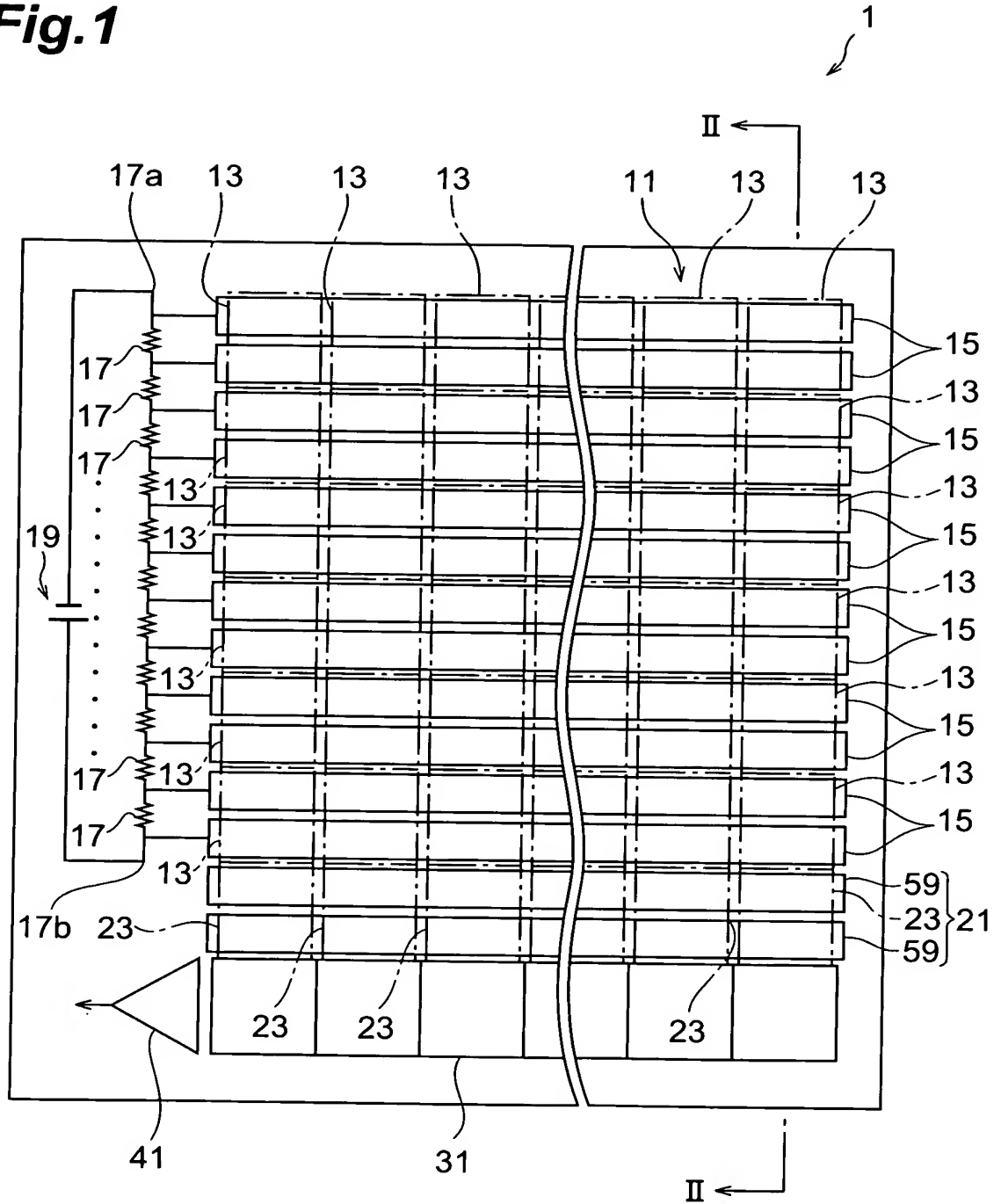


Fig.1



This diagram shows a cross-sectional view of a semiconductor device. The substrate is labeled P-Si. Above the substrate is a layer labeled N, which contains several regions labeled N+. A series of resistors, labeled 15, are connected in series along the top. The resistors are connected to a common line labeled 17. A dashed line labeled 21 indicates a connection point. A label 59a points to a specific resistor. A label 57 points to a contact or wire. A label 55 points to the N+ layer. A label 51 points to the P-Si substrate. A label 53 points to the N layer. A label 55a points to the N+ layer. A label 63 points to the P-Si substrate. A label 65 points to the N layer.

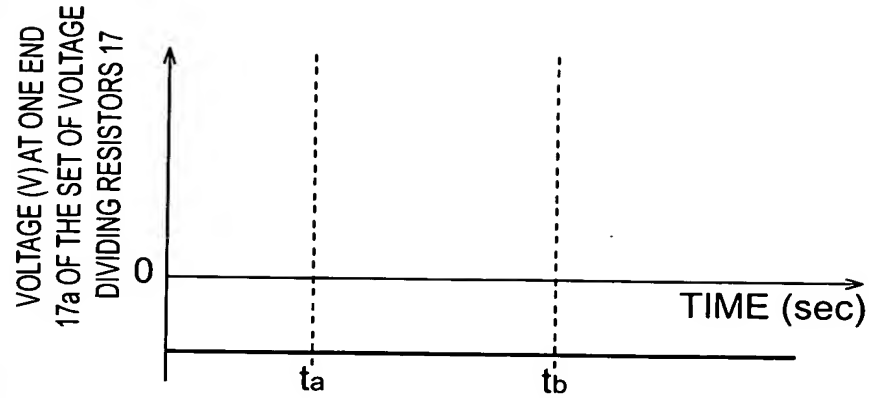
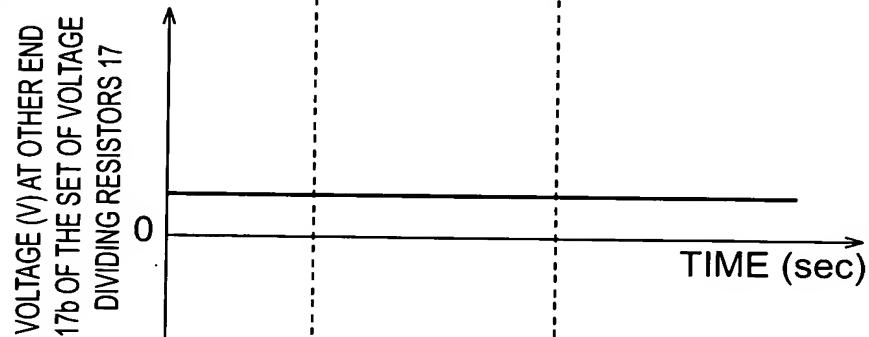
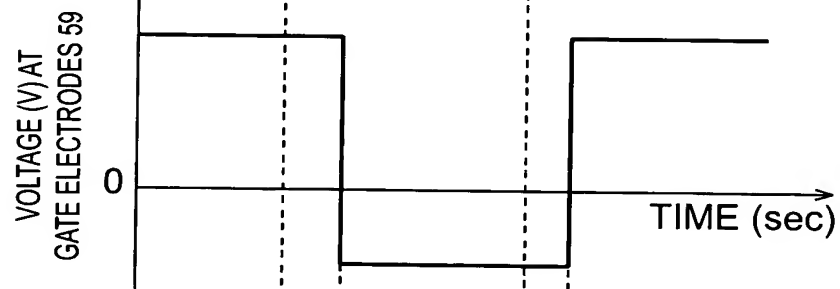
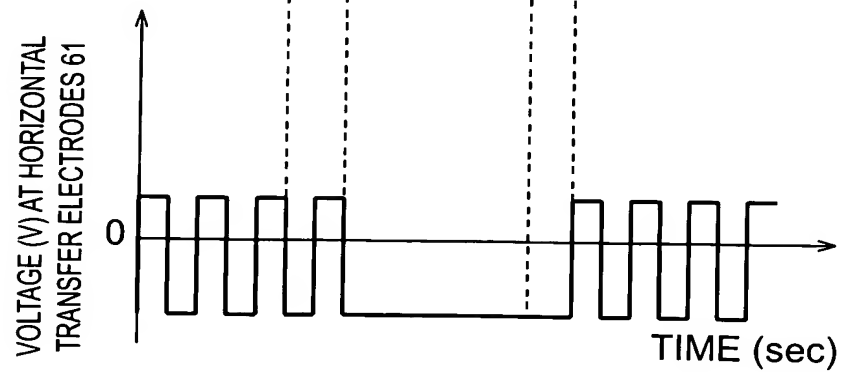
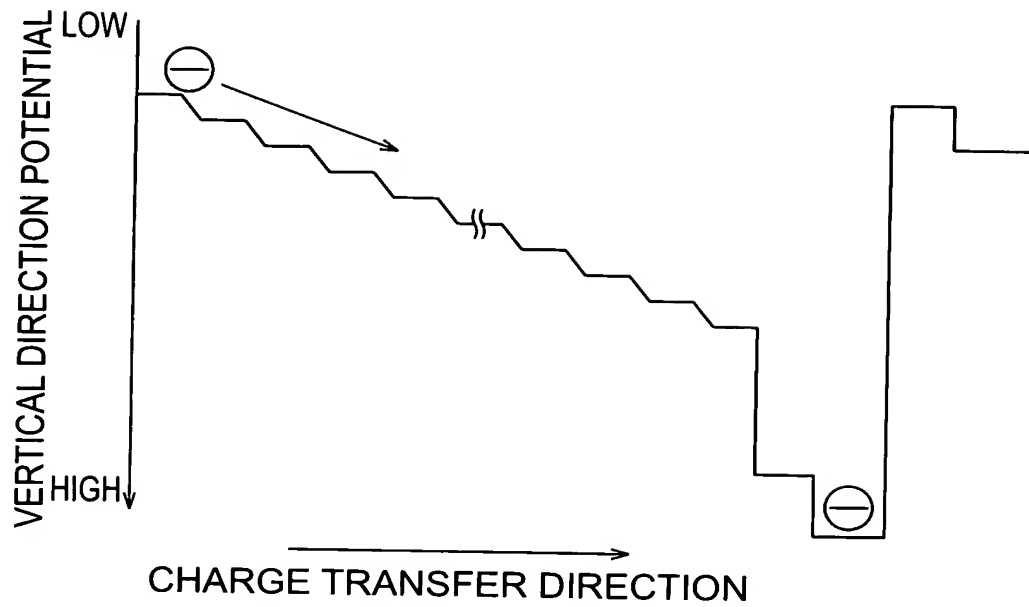
Fig.3A**Fig.3B****Fig.3C****Fig.3D**

Fig.4A**Fig.4B**